GRABLINK FUII[™]

Full Featured Camera Link Frame Grabber - Base, Medium and Full -

The Grablink Full is a **cost-effective full-featured** frame grabber for single Camera Link camera applications. It supports one Base-, Medium- or Full-configuration camera, including 10-tap. The Grablink Full features **on-board processing**, such as LUTs and a Bayer CFA decoder, in addition to a **rich set of IO lines** compatible with a wide range of sensors and encoders, and a **4-lane PCI Express** bus.

This high-end acquisition board targets **high-speed** and **high-resolution** area-scan and line-scan applications such as printing, web and flat panel display inspection, 3D inspection and manufacturing inspection for fast production lines.

- For one Full, Medium or Base Camera Link® configuration camera
 - Including 10-tap configuration cameras
- On-board processing
 LUTs, Bayer CFA decoder
- REDISTERED PRODUCT
- Rich set of digital IO lines compatible with a wide range of sensors and encoders
 - Two high-speed differential RS-422 inputs
 - Four individually-isolated wide-voltage-range inputs
 - Four individually-isolated contact outputs
- Form factor:
 PCI Express x4, full-height, half-length





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GRABLINK FUII"

<u>Support of one Full, Medium or Base Camera Link® configuration camera</u>

Types of cameras supported:

- Monochrome, Bayer CFA, and color RGB
- Progressive area-scan cameras
- Line-scan cameras
 - · Continuous (web) or finite (page) acquisition modes
 - Extensive set of trigger modes to start the acquisition of pages
 - Trigger decimation to reduce the trigger rate
 - Extensive set of line trigger modes to control and synchronize line-scan cameras
 Support of quadrature incremental motion encoders, with optional direction selection
 - and backwards motion cancellation
 - Rate divider and rate converter to adjust the line rate while maintaining synchronization with the motion encoder; non-integer rate conversion ratios are available for fine tuning Device the sense to a sense to a fine during rate.
 - Periodic generator to select a fixed line rate
- From 1-tap to 10-tap configuration cameras
- Full pixel clock range: from 20 MHz up to 85 MHz
- Four fully-configurable upstream control lines
- Serial communication interface (600 ~230400 bit/s)
- 2 Mini Camera Link® SDR connectors



- Image reconstruction: tap re-ordering and horizontal/vertical flipping
- Bayer CFA decoder
- Look-up table transformation
 - Monochrome operation with selectable output bit depth: 8 bits, 10 bits, 12 bits or 16 bit
- RGB and Bayer color operations with selectable output bit depth: 3 x 8 bits, 3 x 10 bits, 3 x 12 bits or 3 x 16 bits
- High-performance 800 megabytes/sec DMA transfers into user allocated memory with 64-bit addressing capability with image cropping capability

128MB on-board memory

Rich set of digital IO lines

- I/O electrical style
 - Two high-speed differential RS-422 inputs
 - Four individually-isolated wide-voltage-range inputs
 - Four individually-isolated contact outputs
 - +5V and + 12V power output with electronic fuse protection
 - One external system IO connector

- I/O functions
 - General purpose usage
 - Trigger source from external signals
 - EndTrigger sources from external signals
 - Line Trigger source from one or a pair of external signals
 - Strobe output delivered through an isolated contact output

<u>MultiCam driver</u>

- · Seamless migration from previous generation of Grablink boards
- Operates on 32- and 64-bit Windows and Linux operating systems
- Camera Link[®] serial communication DLL or COM port emulation

Form factor

• PCI Express x4, full-height, half-length

Ordering Information ORDER CODE DESIGNATION 1622 GRABLINK Full

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